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A Two-Day National level Seminar on  
Nano-scale MOSFET modeling for the  
Low-Power Analog and RF Circuits  
07<sup>th</sup> & 08<sup>th</sup> June 2018

Registration Form

(To be filled in block letters)

Name : .....

Department : .....

Gender : Male / Female

Designation : .....

Organization : .....

Address for Communication: .....

.....

.....

Phone : .....

Email id : .....

Demand Draft Details:

DD No. & Date : .....

Amount : .....

Bank Name : .....

Date:

Signature

**ORGANIZING COMMITTEE**

**Chief Patron**

Dr. P. Appukutty  
Chairman, P. A. Institutions

**Patron**

Dr. Lakshmi Appukutty  
Vice Chairperson, P. A. Institutions

**Organizing Chair**

Dr. T. Manigandan  
Principal

**Conveners**

Dr. D. Ganeshkumar, Prof & Head/ECE  
Mr. N. Arunkumar  
Assistant Professor, Department of ECE

**Co-coordinator**

Mr. N. Senathipathi  
Assistant Professor, Department of ECE

**Department Vision**

To enrich the students, technical knowledge and practical skills in the field of Electronics and Communication Engineering and to nurture highly emulous communication engineers with the power to facilitate the society.

**Department Mission**

To provide quality education and promote research in the field of Electronics and Communication Engineering and thereby rendering continuous service to the society by imbibing leadership skills and moral values in the students.

A Two-Day National level Seminar on  
Nano-scale MOSFET modeling for the  
Low-Power Analog and RF Circuits  
07<sup>th</sup> & 08<sup>th</sup> June 2018

Organized by

**DEPARTMENT OF  
ELECTRONICS AND COMMUNICATION  
ENGINEERING**

(Accredited by NBA)



**P. A. COLLEGE OF ENGINEERING AND  
TECHNOLOGY**

(Accredited by NAAC With 'A' Grade)  
(An ISO 9001-2008 Certified Institution)  
Pollachi - 642 002.

Phone : 04259 - 221386 / 87

Fax : 04259-221386

E-mail : pacollege@yahoo.com

Website : www.pacolleges.org



## ABOUT THE INSTITUTION

P.A. College of Engineering and Technology (PACET) is located on the State Highway (SH-19) about 2 km from Pollachi and 42 km from Coimbatore. The College is in the reach of about 40 minutes drive from Coimbatore airport & railway station and one hour from Tiruppur. The Arul Jothi Charitable Trust promoted by Dr. P. Appukutty, Chairman and Managing Trustee, with 35 years of experience in Technical Education had established this institution in 2008, with a vision to progress and to become a center of excellence in Engineering, Technology and Management through creative and innovative practices in teaching and learning and promoting research and development to produce globally competitive and employable professionals.

This institution is approved by AICTE, affiliated to Anna University Chennai, Accredited by NAAC with 'A' Grade and ISO certification. It offers five UG Programmes (B.E in CSE, ECE, EEE, Mechanical, Civil) and Three PG Programmes (M.E in CSE, VLSI Design and Power Electronics & Drives). The college has strong Industry Institute Partnership Cell for placement, guest lectures and projects

## ABOUT THE DEPARTMENT

The department of Electronics and Communication Engineering established in the year 2008 and it is accredited by NBA, with its dedicated body of well qualified faculty, technical staff and students, is a multi-disciplinary centre of excellence in the specialization of Embedded Systems, VLSI, Communications, Signal & Image Processing, through Education and Research. The Department is well equipped with state-of-the-art laboratories to provoke the students and staff community in academic and research. The department offers four year UG programme in Electronics and Communication Engineering and PG programme in VLSI Design.

## OBJECTIVE OF THE WORKSHOP

The aim of this seminar is about future of the MOS transistor. The participants will have an exposure to Modelling for the low power analog and RF circuits along with different analysis of the same. It aims to stimulate a forum for researches, academicians and industry people from all over the country to disseminate the new research findings and explore innovative technologies for the benefit of society in the field of Electronics.

### FACULTY

The Sessions will be handled by Experts from reputed Institutions in the Information & Communication Engineering Optimization field and Department of Electronics and communication Engineering.

## CONTENTS

This Seminar covers a complete solution for

- Bulk MOS intrinsic models
- SOI/double-gate/multiple-gate/floating-gate MOS models
- Atomic-level/quantum-mechanical compact models
- RF/noise/scalable capacitance/NQS models
- Numerical/TCAD/behavioral/table-based models

### ELIGIBILITY

Students and Faculty from AICTE approved Institutions with relevant background in ECE/EEE/EIE/CSE and IT branches are eligible for the programme.

## IMPORTANT DATES

Last Date for Registration	: 31.05.2018
Intimation of selection	: 01.06.2018
Confirmation by Participants	: 04.06.2018

## BOARDING AND LODGING

Boarding and lodging will be provided to the participants in the college campus. Accommodation is free.

## REGISTRATION FEE DETAILS

Faculty / Research Scholar	: Rs 300/-
Students (UG & P.G)	: Rs 250/-

## DD TO BE DRAWN IN FAVOUR OF

The Principal,  
P.A. College of Engineering and Technology,  
Pollachi-642002

## ABOUT THE LOCATION

The college is situated at Pollachi on the State highway (SH-19) about 45 km from Coimbatore and 60 km from Tirupur. It is well connected by road and rail.

## ADDRESS FOR CORRESPONDANCE

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